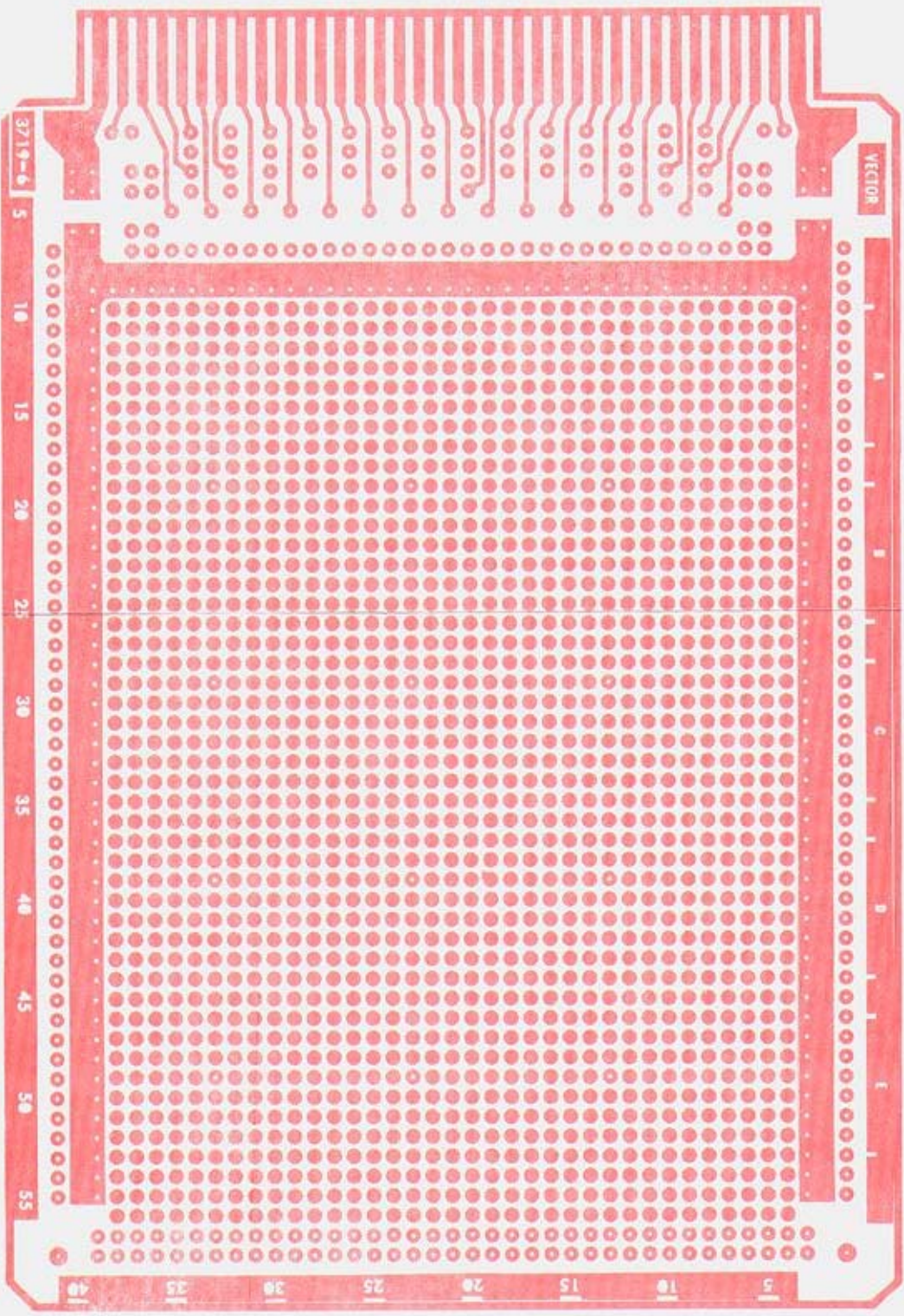


CAUTION: Note that the holes adjacent to the inside edge of the peripheral bus, when looking at the wiring side, are connected to the bus on the component side. Therefore, use these holes only for bus connections.

- NOTES:**
1. Intended for use in non-hostile environments up to 200 volts RMS or 300 volts DC.
 2. Where tin coated circuitry exists, a small percentage of the holes may have solder blockage. This is usually a light "skin" easily penetrated by component leads. In some cases a soldering iron may be required.
 3. In any plug contact area on either side of Plugboard, use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry and using them could cause shorting.
 4. Before pressing terminals into board, position (rotate) terminals to maximize the clearance between the widest part of the terminal and the nearest adjacent conductor.



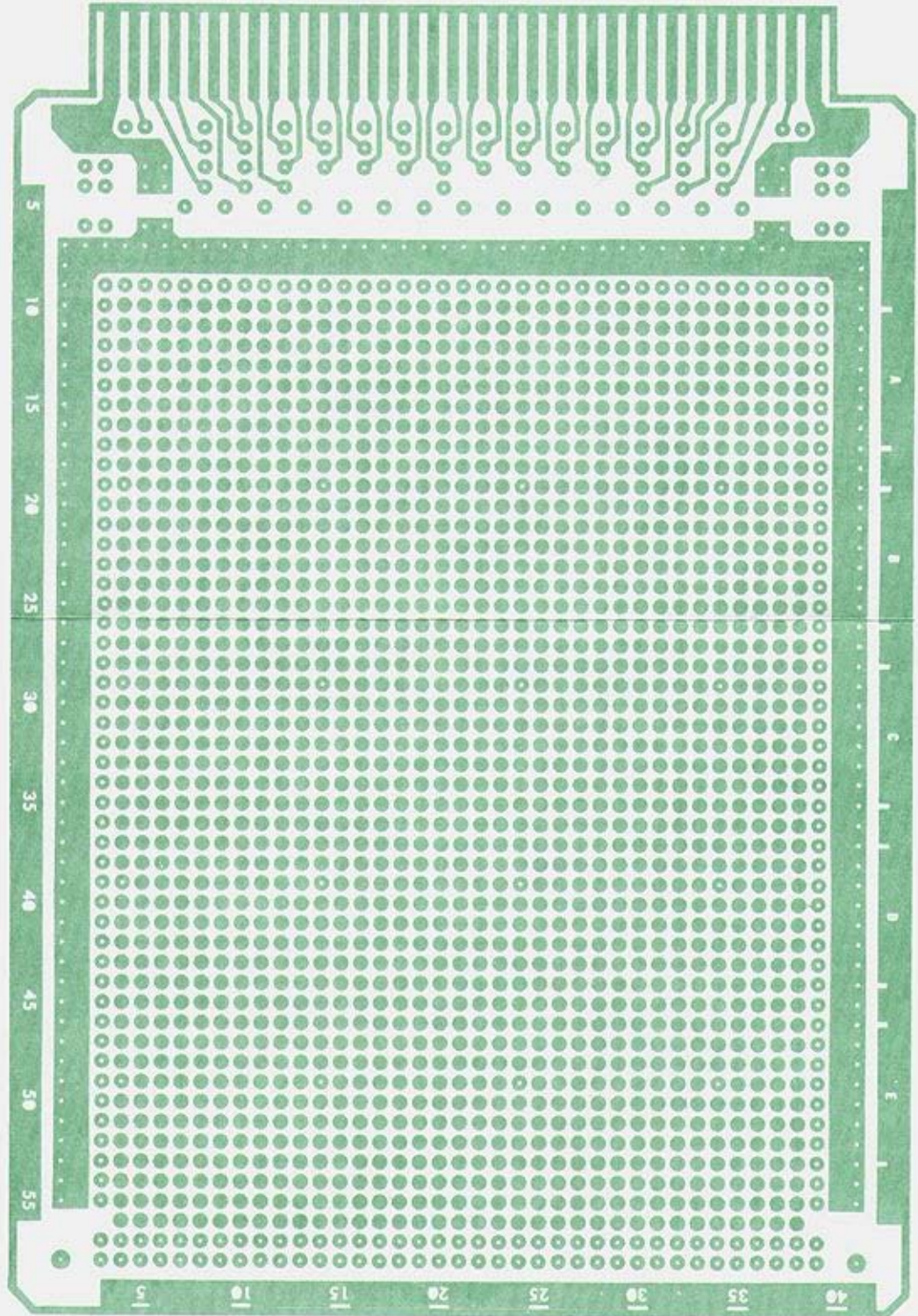
3719-6
COMPONENT SIDE
LAYOUT PAPER



VECTOR PAD/HOLE PLUGBOARD
PATTERN 0.043" DIA. HOLES
0.10" x 0.10" GRID

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3719-6
WIRING SIDE
LAYOUT PAPER



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0.10" x 0.10" GRID