

Technical Brief

February 17, 2005

TB442.0

Using the Evaluation Board

EL7534 is a high efficiency 600mA synchronous step-down PWM regulator in a tiny MSOP10 package. The internal compensation makes it possible for the full-featured 600mA converter to occupy less than 0.18in² of PCB area with all components on one side.

1. Output Adjustment

Use R_1 and R_2 to adjust output voltage according to the following formula:

$$V_{O} = 0.8 \times \left(1 + \frac{R_2}{R_1}\right)$$

The evaluation board is set to V_{O} = 1.8V.

2. Chip Enable

When EN pin is pulled to Ground, the regulator is disabled. It consumes less than 1μ A of current. R₅ can be connected to V_{IN} directly if this function is not needed, reducing the component counts.

3. RSI/POR Function

When powering up, the open-collector Power-On-Reset output holds low for 100ms after V_O reaches the preset voltage. When a reset signal RSI is issued, POR goes to low for the same period of time while the output power is still on (Please refer to the datasheet for the timing diagram). When the function is not used, connect RSI to ground and leave R₄ open, further reduce the total components counts and the overall PCB area to less than 0.18in².

The POR output also serves as a 100ms delayed Power Good signal when the pull-up resister R_4 is installed. The RSI pin needs to be directly (or indirectly through R_6) connected to Ground for this to function properly.

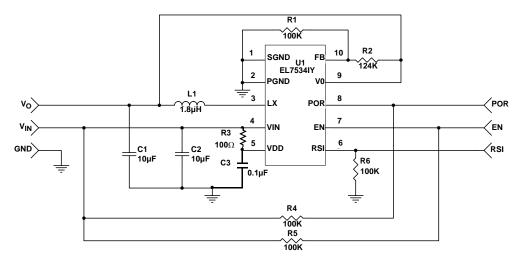


FIGURE 1. DEMO BOARD SCHEMATIC

Evaluation Board Bill of Materials

REFERENCE DESIGNATOR	VALUE	PACKAGE	MANUFACTURER	MANUFACTURER'S PART NUMBER	MANUFACTURER'S PHONE NUMBER
C ₁ , C ₂	10µF/MLCC, X7R	1206	TDK	C3216X7R1A106K	847-803-6100
C ₃	0.1µF	0603/0402	Any		
L ₁	1.8µH		Coilcraft	1008PS-182M	847-639-6400
R ₂	124K, 1%	0603/0402	Any		
R ₁ , R ₄ , R ₅ , R ₆	100K, 1%	0603/0402	Any		
R ₃	100Ω, 1%	0603/0402	Any		
U ₁	EL7534IY	MSOP10	Intersil	EL7534IY	888-ELANTEC

1

Demo Board Layout (Actual Size - 3" x 2")

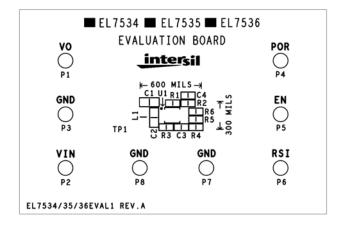


FIGURE 2. SILKSCREEN

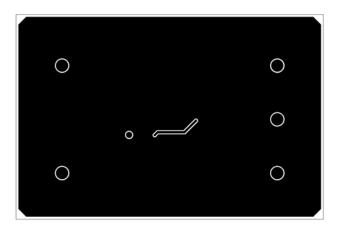


FIGURE 4. BOTTOM LAYER

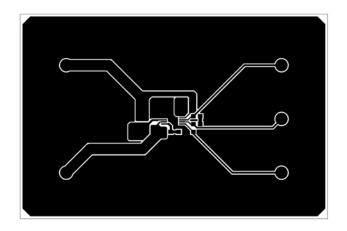


FIGURE 3. TOP LAYER

All Intersil U.S. products are manufactured, assembled and tested utilizing ISO9000 quality systems. Intersil Corporation's quality certifications can be viewed at www.intersil.com/design/quality

Intersil products are sold by description only. Intersil Corporation reserves the right to make changes in circuit design, software and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by Intersil is believed to be accurate and reliable. However, no responsibility is assumed by Intersil or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Intersil or its subsidiaries.

For information regarding Intersil Corporation and its products, see www.intersil.com